

## Description

## xDSL Line Tester

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The present invention relates to a line tester for xDSL, particularly ADSL (Asymmetric Digital Subscriber Line), installations for checking connection setup between a PC connected to an xDSL connection socket and the exchange.

The appearance of the World Wide Web (WWW) has been accompanied by a continually increasing requirement for high data transfer rates over telephone lines. Complex information supplied on the Internet and new areas of communication, such as Video-on-Demand, Video Conferencing or teleworking, require very high transfer rates and hence new transmission techniques. A normal modem on a normal analogue telephone line can achieve a data transmission rate of no more than 56 kBits per second. An ISDN line allows an already somewhat faster data transfer rate of 64 kBits per second to be achieved. "Channel concentration" makes it possible to double the transmission speed, but requires a higher cost outlay. ISDN, channel concentration and software compression had initially exhausted the options for increasing the data transmission rate.

Since the very start of the 90s, however, there has been a technical solution which allows far higher transfer rates using the conventional telephone network and is called ADSL (Asymmetric Digital Subscriber Line). ADSL is a protocol from the xDSL family. A common feature of all xDSL variants is that they use the conventional copper line network, and the DSL signals thus use the same lines as the conventional telephone signals. In this case, however, DSL uses a different frequency range than the telephone. The

analogue telephone signal, which is also abbreviated to POTS (Plain Old Telephone Signal), operates in the range from 0 to 4 kHz, while DSL transmissions use the range from approximately 25 kHz upwards. The particular  
5 feature of ADSL technology is that it affords different data transmission speeds in the two directions of transmission, which is the origin of the term "asymmetric". Toward the user (downstream), it is theoretically possible for up to 8 Mbit/s to flow, and  
10 in the opposite direction (upstream) the figure is up to 1 Mbit/s.

Since the two copper wires existing in any conventional telephone line are used, installation generally  
15 involves just a second TAE socket for the computer line being installed next to the TAE socket which already exists. The computer is connected to the second TAE socket via a DSL modem. Upstream of the branch to the two TAE sockets, the line contains a "splitter" which  
20 separates the DSL signals from the received data stream and forwards them to the modem.

An ADSL line is installed on the analogue telephone line (POTS) on a basis of a standard from the ITU  
25 (International Telecommunication Union) which is called ANNEX A. In Germany and in some neighboring countries with widespread ISDN use, the installation can also be effected on the ISDN line, in which case the ANNEX B standard from the ITU is the basis used. ANNEX A and B  
30 differ essentially only in the frequency range. The ADSL line is usually installed by a service provider, whose task is to ensure correct operation and the assured features. The Internet access is provided by another company, the ISP - Internet Service Provider.  
35 This means that the installer needs to ensure that the ADSL line works, i.e. can set up a connection, after he has set it up. This has entailed a relatively high level of complexity to date, however, namely a PC with

a modem and software or just a modem and an ADSL test unit or an ADSL test unit with an inbuilt modem. Another problem in this context is that there are various types of modem, which cannot communicate with every exchange.

A common feature of all known test methods is that it is necessary to use relatively expensive test units on the basis of ADSL testers with protocol software, Ethernet interface etc., and the user also needs to have the appropriate level of training.

A known test unit is a "PING tester", for example. This also sends a request to a server at the protocol level, and the server then acknowledges this request with the "PING". For this case too, the full technology including software is required, which means that it is not possible to manufacture simple, inexpensive test units on this basis.

It is accordingly an object of the present invention to specify an xDSL, particularly ADSL, line tester which is of simple design and is easy to implement and to use.

This object is achieved with the features of patent claim 1. Advantageous developments and refinements can be found in the subclaims.

The invention is based on the fundamental consideration that the person setting up the line, and the customer after installation, initially require only information about whether the ADSL line which has been installed can be used to set up a connection to the next exchange (DSLAM, Digital Subscriber Line Access Multiplexer) - regardless of the data rate which can be achieved or other information flowing at a protocol level. A significant aspect for the invention is that, in the

course of connections setup by the modem, the first contact is not made at the protocol level.

The first information which the modem sends to the  
5 DSLAM comprises the emission of one of a plurality of possible frequency signals, referred to as R tones, which are specified in the aforementioned specifications ANNEX A or B. These frequency signals need to have a particular shape and duration on the  
10 basis of said specifications. If the DSLAM now receives these frequency signals and recognizes their shape and duration as being correct, the DSLAM responds to this by emitting a further frequency signal from a plurality of possible frequency signals, namely the "C tones", as  
15 acknowledgement signals.

A fundamental concept of the present invention now involves these specified frequency signals, that is to say R tones in particular, being generated at a purely  
20 physical level, and the frequency signals returned by the DSLAM being detected and converted into an OK signal.

The xDSL line driver in accordance with the invention  
25 thus contains

- means for generating at least one polling frequency signal of prescribed shape and duration which is intended for the connection test,
- means for transmitting the frequency signal onto  
30 the line,
- means for detecting at least one response frequency signal transmitted by a remote station on the line in response to the polling frequency signal, and
- means for signaling setup of a connection to the  
35 remote station on the basis of detection of the response frequency signal.

In this case, the polling frequency signal is

preferably "provided" by an R tone based on the specification published by the ITU (International Telecommunication Union), and the generating means are designed for generating this R tone.

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In addition, the detection means are preferably designed for detecting at least one C tone based on the aforementioned specification.

10 The generating means can be provided by a frequency generator such as a sine wave generator actuated or keyed in a suitable manner, so that it sends an R tone of the specified shape and duration to an output transformer. In addition, a power amplifier can be  
15 arranged between the frequency generator and the output transformer.

The detection means can have a high pass filter for isolating the response frequency signal and an  
20 integrator. These can have a power amplifier arranged between them. Following integration, the signal can be supplied to a Schmitt trigger.

The electronic circuit in the ADSL line tester in  
25 accordance with the invention can contain a central processor unit (CPU) for controlling the sequences. The CPU is connected by means of an output line to a transmission path containing the frequency generator and is connected by means of an input line to a  
30 reception path containing the high pass filter and the integrator.

A specific exemplary embodiment of the electronic circuit in an ADSL line tester in accordance with the  
35 invention is specified below with reference to the single figure of the drawing.

The figure's block diagram of an electronic circuit in

an ADSL tester in accordance with the invention can be accommodated in an easily portable housing. The outer wall of the housing contains, inter alia, a pushbutton switch 11 and two light emitting diodes 12 and 13. The housing is connected to an TAE connector which can be plugged into the TAE connection socket for an ADSL line. The voltage supply 10 can be taken from a battery or from a chargeable storage battery.

10 The pushbutton switch 11 is used by a user to start the test procedure. As a result of the pushbutton switch 11 being operated, the CPU 1 is prompted to send an output signal to the frequency generator 2 on the transmission path. The frequency generator 2 then generates a  
15 specified polling frequency signal, particularly an R tone, which is amplified in the power amplifier 3 and is sent to the output transformer 4. The latter transmits the frequency signal to the line which is coupled by means of the TAE connector. Operation of the  
20 start signal using the pushbutton switch 11 can be indicated by the red light emitting diode 13.

When a response frequency signal, particularly a C tone, is received, it is routed via the reception path  
25 and is isolated from any other signal components by the high pass filter 5. The signal is then amplified in the power amplifier 6 and is supplied to the integrator 7. The output signal from the integrator 7 can also be supplied to a Schmitt trigger (not shown) and can then  
30 be detected by the CPU 1. If the signal detected by the CPU 1 is adequate, i.e. exceeds particular prescribed level values, then the CPU 1 prompts output of a signal for actuating the light emitting diode 12, which means that the latter's lighting up indicates to the user  
35 that the connection to the DSLAM has been set up successfully.

The CPU 1 contains programmed values for the shape and

duration of the R tone which is to be emitted.

- The voltage supply 10 is connected to a DC voltage regulator 15 which outputs a 5 V DC voltage signal. The
- 5 CPU 1 is connected to a switch 14 which can supply the 5 V DC voltage signal to the frequency generator 2, to the integrator 7 and to the power amplifiers 3 and 6, for example after the tester has been switched on.
- 10 The exemplary embodiment described above relates to an ADSL line tester. The invention can likewise be applied in principle to other line installations from the xDSL family, however.